**PATENT** 

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At page 143, line 26, after ".", insert --When the RU sends training data, it sets tap coefficients of its precode equalization filter such as filter 563 in Figure 33 to values that cause the precode equalization filter to not predistort the training data signal.--

At page 144, line 8, delete the sentence "The RU then sets these final tap weight coefficients into FFE and DFE equalizers within the precode equalizatin filter 563 in the transmitter of Figure 33, as symbolized by step 1124." and substitute, --To calculate the new coefficients for the precode equalization filter 563 in the RU transmitter of Figure 33, the old coefficients of the RU precode filter FFE and DFE equalization filter are convolved with the new coefficients FFE and DFE coefficients which the central unit modem symbol equalizer circuit converged on to derive new coefficients. These new coefficients are then set into the RU precode filter.--

At page 144, line 17, delete "tap weight coefficients of FFE and DFE" and substitute --the main tap of said FFE equalizer 921 in Figure 50 to one and sets the side tap coefficients of the FFE equalization filter 921 and the DFE equalization filter 929 in Figure 50 to zero --. At line 18, delete "equalizers 765 and 820, respectively to one".

At page 145, lines 10 through 13, delete the sentence "After convergence, the RU CPU reads the final tap weight coefficients for the FFE equalizer 765 and the DFE equalizer 820 via bus 833 and, in this alternative embodiment, sends these tap weight coefficients to the FFE/DFE circuit 764 in the RU receiver of Figure 30 via bus 822, as symbolized by step 1132." and substitute the following --After convergence, the RU CPU reads the final tap weight coefficients for the FFE equalizer 765 and the DFE equalizer 820 via bus 833 and calculates new tap weight coefficients for the FFE and DFE filters of the CE circuit 764 in the RU receiver of Figure 30 by convolving the old

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